

CLAIMS

What is claimed is.

1 1. A polymer memory device comprising:
2 a first electrode disposed on a substrate;
3 a first organic adhesion layer disposed over the substrate and over the first
4 electrode;
5 a ferroelectric polymer structure disposed above and on the first organic adhesion
6 layer;
7 a second organic adhesion layer disposed above and on the ferroelectric polymer
8 structure; and
9 a second electrode disposed over the second organic adhesion layer.

1 2. The polymer memory device according to claim 1 further comprising:
2 a first protective film disposed between the first electrode and the ferroelectric
3 polymer structure; and
4 a second protective film disposed between the ferroelectric polymer structure and
5 the second electrode.

1 3. The polymer memory device according to claim 1, wherein layers between the
2 electrodes further consist essentially of:
3 the first organic adhesion layer;
4 the ferroelectric polymer structure;

5 the second organic adhesion layer; and
6 an upper protective film disposed above and on the organic adhesion layer.

1 4. The polymer memory device according to claim 1, wherein the first and second
2 organic adhesion layers have a thickness in a range from about 25 Å to about 200 Å.

1 5. The polymer memory device according to claim 1, wherein the first and second
2 organic adhesion layers have a thickness in a range from about 30 Å to about 80 Å.

1 6. The polymer memory device according to claim 1, wherein the ferroelectric
2 polymer structure further comprises:
3 a single, crystalline ferroelectric polymer layer disposed over the substrate.

1 7. The polymer memory device according to claim 1, wherein the ferroelectric
2 polymer structure further comprises:
3 a spin-on ferroelectric polymer layer disposed over the substrate.

1 8. The polymer memory device according to claim 1, wherein the organic adhesion
2 layers further comprise:
3 a hexamethyldisilazane composition.

1 9. The polymer memory device according to claim 1, wherein the ferroelectric
2 polymer structure further comprises:

3 a first crystalline ferroelectric polymer layer disposed over the first organic
4 adhesion layer;
5 a spin-on ferroelectric polymer layer disposed over the first crystalline
6 ferroelectric polymer layer; and
7 a second crystalline ferroelectric polymer layer disposed over the spin-on polymer
8 layer.

1 10. The polymer memory device according to claim 1, wherein the first and second
2 organic adhesion layers exhibit a morphology characteristic of a spin-on formation process.

1 11. The polymer memory device according to claim 1, wherein the first and second
2 organic adhesion layers exhibit a morphology characteristic of a Langmuir-Blodgett deposition
3 process.

1 12. The polymer memory device according to claim 1, wherein the first organic
2 adhesion layer exhibits a morphology characteristic of a spin-on formation process, and the
3 second organic adhesion layer exhibits a morphology characteristic of a Langmuir-Blodgett
4 deposition process.

1 13. A cross-point matrix polymer memory structure comprising:
2 a first aluminum or copper electrode disposed on a substrate;
3 a first refractory metal nitride or oxide protective film disposed above and on the
4 first electrode;

5 a first organic adhesion layer disposed above and on the first refractory metal
6 nitride or oxide protective film;
7 a ferroelectric polymer structure disposed over the substrate and the first
8 protective film;
9 a second organic adhesion layer disposed above and on the ferroelectric polymer
10 structure;
11 a second refractory metal nitride or oxide protective film disposed over the
12 ferroelectric polymer structure; and
13 a second aluminum or copper electrode disposed above and on the second
14 refractory metal nitride protective film.

1 14. The cross-point matrix polymer memory structure according to claim 13, wherein
2 the organic adhesion layers comprise:
3 a hexamethyldisilazane composition.

1 15. The cross-point matrix polymer memory structure according to claim 13, wherein
2 the organic adhesion layers have a thickness in a range from about 25Å to about 200 Å.

1 16. The cross-point matrix polymer memory structure according to claim 13:
2 wherein the organic adhesion layers further comprise a hexamethyldisilazane
3 composition; and
4 wherein the ferroelectric polymer structure further comprises a polymer selected
5 from $(\text{CH}_2\text{-CF}_2)_n$, $(\text{CHF-CF}_2)_n$, $(\text{CF}_2\text{-CF}_2)_n$, α -, β -, γ -, and δ -phases thereof, $(\text{CH}_2\text{-CF}_2)_n$ -

6 (CHF-CF₂)_m copolymer, α-, β-, γ-, and δ-phases of (CH₂-CF₂)_n-(CHF-CF₂)_m copolymer,
7 and combinations thereof.

1 17. A process of making a storage device comprising:
2 forming a first electrode on a substrate;
3 forming a first organic adhesion layer over the substrate and over the first
4 electrode;
5 forming a ferroelectric polymer structure over the first organic adhesion layer;
6 forming a second organic adhesion layer above and on the ferroelectric polymer
7 structure; and
8 forming a second electrode above the second organic adhesion layer.

1 18. The process according to claim 17, wherein forming a first organic adhesion layer
2 over the substrate and over the first electrode is carried out by spin-on deposition.

1 19. The process according to claim 17, wherein forming a ferroelectric polymer
2 structure over the first organic adhesion layer is carried out by spin-on deposition.

1 20. The process according to claim 17, wherein forming a second organic adhesion
2 layer above and on the ferroelectric polymer structure is carried out by spin-on deposition.

1 21. The process according to claim 17, wherein forming a first organic adhesion layer
2 and forming a second organic adhesion layer further comprise:

3 depositing at least one hexamethyldisilazane composition over the substrate.

1 22. The process according to claim 17, further comprising:
2 forming a first protective film over the first electrode; and
3 forming a second protective film above and on the second organic adhesion layer.

1 23. The process according to claim 17, wherein forming a ferroelectric polymer
2 structure over the first organic adhesion layer further comprises:
3 forming a first crystalline ferroelectric polymer layer over the first organic
4 adhesion layer;
5 forming a spin-on ferroelectric polymer layer over the first crystalline
6 ferroelectric polymer layer; and
7 forming a second crystalline ferroelectric polymer layer over the spin-on polymer
8 layer.

1 24. The process according to claim 17, wherein forming a first organic adhesion layer
2 and forming a second organic adhesion layer each further comprises:
3 spinning on the adhesion promoter over the substrate for a period from about 5
4 seconds to about 20 seconds and in a rotational range from about 300 rpm to about 6000
5 rpm.

25. The process according to claim 17, wherein the ferroelectric polymer structure is selected from polyvinyl and polyethylene fluorides, polyvinyl and polyethylene chlorides, polyacrylonitriles, polyamides, copolymers thereof, and combinations thereof

26. The process according to claim 17, further comprising:
forming a first protective film above and on the first electrode; and
forming a damascene structure in the substrate from the first electrode and the first protective film.

27. A memory system comprising:
a substrate disposed on a physical interface for a host;
a memory article disposed on the substrate, the memory article comprising:
a first electrode disposed on a substrate;
a first organic adhesion layer disposed over the first electrode;
a ferroelectric polymer structure disposed over the first organic adhesion layer;
a second organic adhesion layer disposed over the ferroelectric polymer structure; and
a second electrode disposed above and on the second organic adhesion layer;
a signal interface for communication from the memory article to the host; and
a host.

1 28. The memory system according to claim 27, wherein the physical interface is
2 configured to a host interface that is selected from a PCMCIA card interface, a compact flash
3 card interface, a memory stick-type card interface, a desktop personal computer expansion slot
4 interface, and a removable medium interface.

1 29. The memory system according to claim 27, wherein the organic adhesion layers
2 further comprise:
3 a hexamethyldisilazane composition.